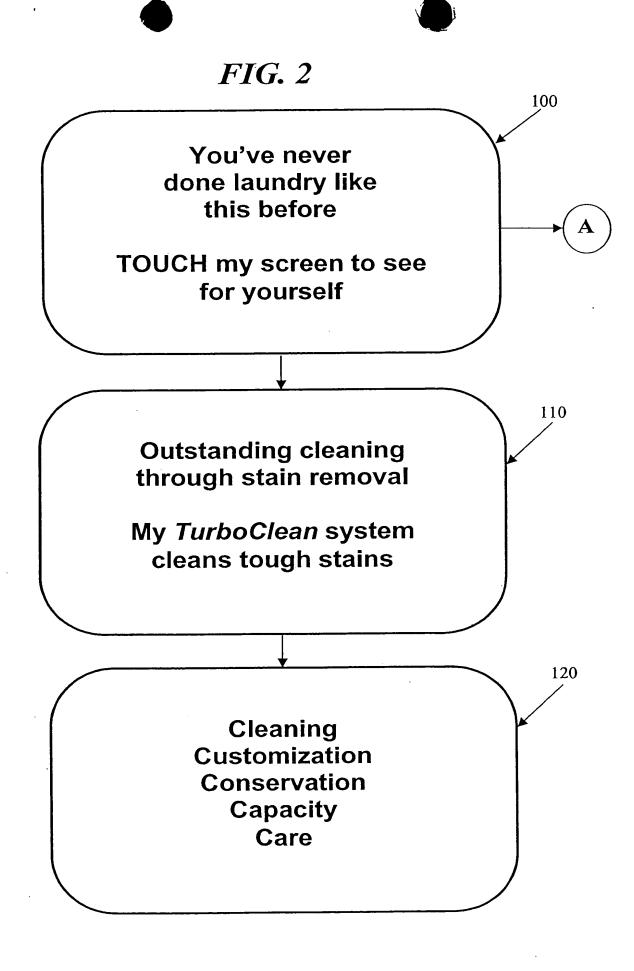
FIG. 1 90 85 CPU Tub Drive Controls Memory 75-Cycle Controls Volatile Memory 98-95 50 53 59-*2*0



*FIG. 3A* 

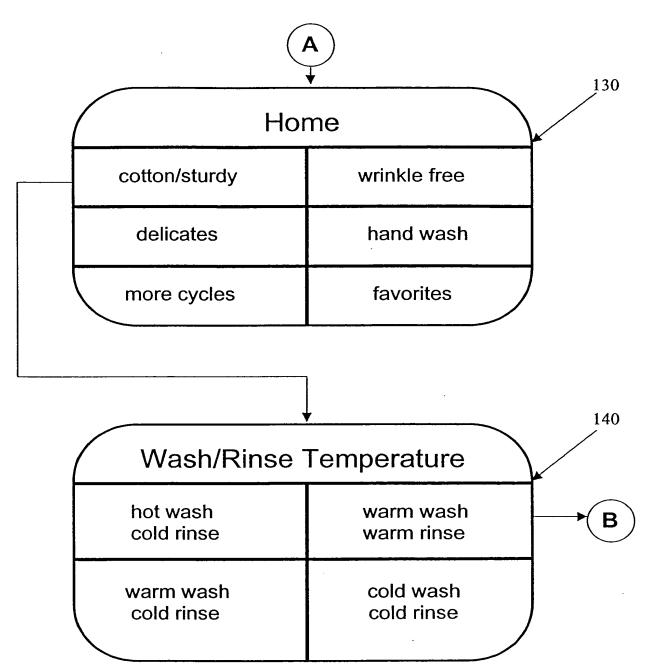


FIG. 3B

